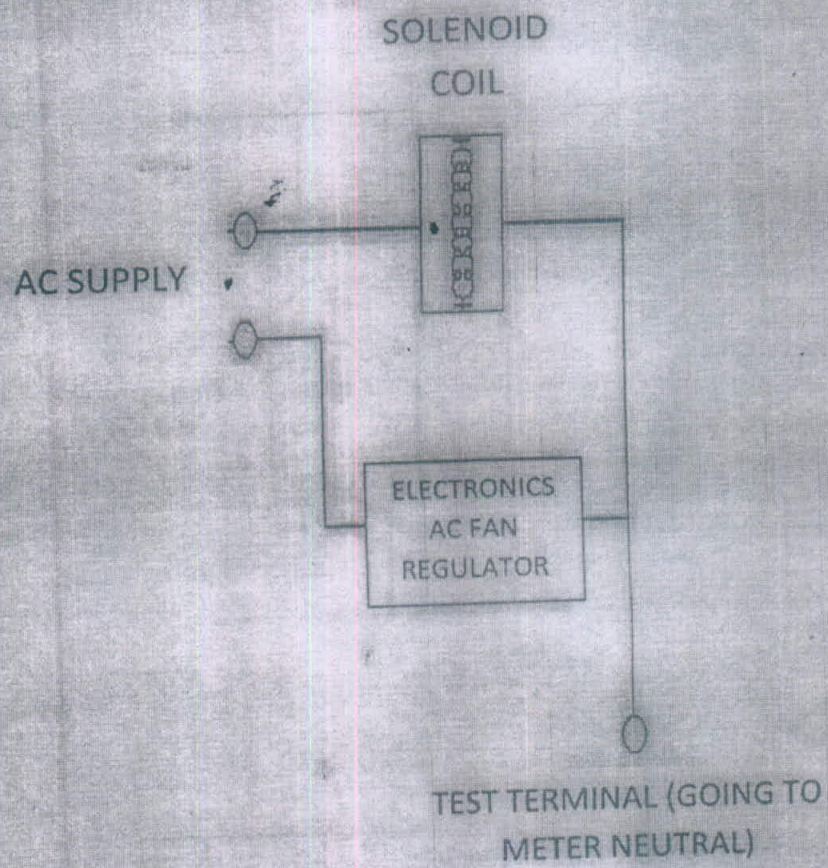


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Annexure - III

CIRCUIT DIAGRAM FOR TEST JIG TO INTRODUCE CHOPPED AC IN NEUTRAL



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Annexure - IV

CIRCUIT DIAGRAM FOR DC PULSATING SIGNALS IN NEUTRAL

